# **Sensor Platform**

# PSU Capstone Project

# WPR

Last Week: 1/30 – 2/6

Next Week: 2/7 – 2/13

# Steve Peirce

## Last week:

* Clarified another revision with Golriz and Professor Greenberg for board layout. Added USB TVS, P-Channel MOSFET for SD card reset.
* Decided on USART for serial comm via GUI.
* Finalized BOM after board revision meeting.
* Attempted to convince teammate to read documentation for the processor repeatedly.
* Attempted to clarify project goals and methodologies with teammate.
* Continued working on SDIO for data logging.
* Continued research on USART protocol for programming.

## Next week:

* Complete SDIO data-logging ability.
* Complete Accelerometer I2C library.
* Select VOC sensor.
* Order parts.
* Send board to OSHPark for fabrication.
* Continue working on USART comm protocol, (ideally, complete).
* Create test plan for hardware.
* Reassess job dispersion with temmates.
* Meet with team, Dr. Zurk and Professor Faust.

## Issues:

* SDIO incomplete
* USART serial incomplete
* Teammate refuses to read documentation.
* Teammate refuses to provide links, documentation or examples for decisions on design parameters.
* Board needs to sent to FAB.